Course Goals
To introduce the latest VLSI Design Technologies and hardware and software packages which will be introduced in this course are:

- Xilinx ISE 9.1 for FPGA digital design, DSP design and implementation, Electrical and instrumentation applications using Spartan Virtex.
- Embedded system application using Xilinx EDK and Spartan/Virtex.
- Active HDL for VHDL/Verilog digital design and simulation.
- Tanner tool for IC design.
- Process Tool for silicon using Synopsis CAD.

Course Contents
- Overview of VLSI/ASIC design and methodology.
- Course on VHDL/Verilog, Spice.
- Architectural overview of FPGA and CPLDs.
- The design and fabrication process (Full Custom and Semi Custom).
- Cell Library Design.
- Analog/Mixed signal design methodology.
- Testing/Mixed signal design methodology.
- Testing and Verification issues of VLSI Design.

Prerequisites
Students are expected to know the fundamentals of analog and digital circuits. Students should have completed at least 4th Semester of BPUT.

Course Fees :
For Students: Rs. 7,000/-
For Industry sponsored candidates: Rs. 8,500/-

Course Goals
To prepare you to speak English fluently by training you to produce the sounds of the language in the right way. After the end of this course the students will master the basic elements of English pronunciation and improve their production of the spoken language. Practice material will be given and particular difficulties the speakers will be noted and remedial exercises provided. The training will be provided through Hi-Class SW software.

Course Contents
- The Basics sounds and sound groups of English(sounds & consonants)
- Problems in pronunciation
- Stress & Rhythm units
- Intonation. Listening & Speaking Practice
- Introduction to GRE and TOEFL
- Introduction to English Certificate(BEC)
- Vocabulary Practice
- Group Discussions.

Prerequisites
For all BE, B.Tech., MCA, M.Tech. and MBA students.

Course Fees :
For Students: Rs. 5,700-
For Industry sponsored candidates: Rs. 8,200/-

Course Goals
To prepare you for the first paper (Exam # 1Z0-043) of OCA@Oracle Certified Associate certification exam. As this course covers the first 5 chapters of the Oracle 9i Explore, this will also help in designing back-ends for database related projects.

Course Contents
- Writing Basic SQL Select Statements
- Restricting and Sorting Data
- Single Table Joins
- Displaying Data from Multiple Tables with Joins
- Aggregating Data Using Group Functions.
- Creating Indexes
- Producing readable Output with iSQL*Plus
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- Creating Indexes
- Producing readable Output with iSQL*Plus

Prerequisites
You must have knowledge of any one high level programming language like C, Pascal, or Fortran and have some experience to database fundamentals.

Course Fees :
For Students: Rs. 7,000/-
For Industry sponsored candidates: Rs. 8,500/-