

ABOUT BERHAMPUR CITY

The city of Berhampur is quite well known as Silk City for its traditional handicrafts and Silk garments. The m a j o r attraction for tourists is



Gopalpur-on-sea which is 11 Kms away from Berhampur. **It is famous for one of the most pristine beaches of Orissa, where swaying (casuarinas, coconut) trees and sand dunes which separates the beach from blue waters act as the perfect setting for peace and tranquility. Berhampur is also famous for its temples and creeping attractions like hot water spring with medicinal benefits (Taptapani).** The climatic condition is almost favorable as the day time temperature is max 35° C with occasional monsoon rains.

COURSE INSTRUCTORS

Experts from IISc, IIT's, NIT's, IIIT's, NIST and Persons from Industry (having expertise in tools) would deliver the lectures on latest technologies, **in association** with NIST VLSI Group.

ELIGIBILITY OF THE APPLICANT

Faculty members from AICTE recognized Engineering Institute, Autonomous College and Fresher from Industry having one to two years of experience in relevant discipline are eligible to participate.

HOW TO APPLY

Xerox / typed application form duly filled and **forwarded** by the Head of the Institute should

reach on or before **10th Aug, 2009**. A copy of the same can be sent to satyabratadas.m@gmail.com. You can also download the application form from the website www.nist.edu. **No registration fee is required.** The applications will be short listed by first come first serve basis and the candidates would be intimated through e-mails only. So all are requested to provide the valid mail ID's in the registration form. Please call at 094380-06890 for more information.

TA/DA & ACCOMMODATION

The participants will be provided with free boarding and lodging and will get TA (as per AICTE norms to and fro fare on production of ticket) and local transport. Twin shared basis accommodation will be provided to the participants.

ORGANIZING COMMITTEE

Chairperson

Prof. Sangram Mudali, Director, NIST

Co-Chairperson

Dr. Ajit Kumar Panda, Dean, NIST

Advisory Committee Members

Prof. Nirranjan Das

Prof. S. P. Pati (Emeritus fellow)

Prof. J. Majhi (Emeritus fellow)

Prof. Geetika Mudali

Dr. A. K. Padhy

Dr. P.C. Biswal

Dr. Sudhir K. Panigrahy

Dr. S. K. Tripathy

Dr. Motahar Reza

Dr. Siddharth Panda

Mr. Siddhartha Neelamani

Mr. Shom P. Das

Mr. Bhawani S Patnaik

Mr. Abhiro Mukherjee

Organizing Committee

Mr. M. Suresh

Mr. Mihir Hota

Mr. T. R. Lenka

Mr. Rajesh Kumar Dash

Dr. Maifuz Ali

Dr. Satyasopan Mahato

Mr. P. K. Patra

Mr. Sudhakar Das

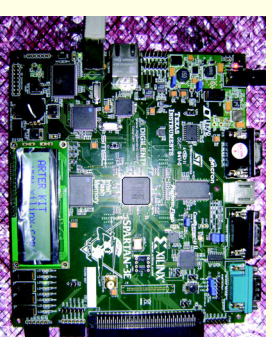
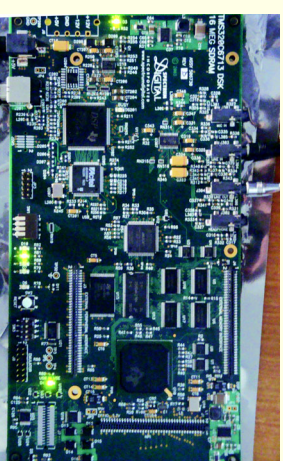
Mr. Prabhudutta Pradhan

Mr. Mukesh Sukla

Mr. Mitu Baral

Ms. Runa Kumari

Ms. Malabika Patnaik



Address for Correspondence:

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Orissa, India

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AICTE Sponsored



Faculty Development Program

On

**Advanced Digital Signal
Processing Implementation
Using FPGA & DSP Processor**

25th to 31st August 2009

Co-ordinator

Satyabratada Das



Organized by



**National Institute of Science &
Technology**

Palur Hills, Berhampur - 761008

Orissa

Introduction & Objective of the program:

For the last few decades, Signal Processing is considered as an important component in every emerging field such as Remote Sensing, Biomedical, Seismology, Wireless Communications, Multimedia, Space & Earth Exploration and Military applications etc. Henceforth, Colleges and universities worldwide run this course for Electrical, Electronics, Computer Science and related disciplines. Keeping in view of the current requirements of industries and research organizations, the course has been designed in such a way that the faculties from different institutes all over the country can learn and get exposed to various implementation aspects of Advanced DSP algorithms in FPGA and DSP processors for different applications. The course will also benefit young researchers and faculties as well by digging out the current research possibilities in multimedia processing and wireless communications through lectures and lab. sessions delivered by eminent professors and experts from industries.

Topics to be covered

1. Introduction to FPGA
2. Introduction to Code Composer Studio & DSP processor
3. Multirate Digital Signal Processing
4. 2-D FIR & IIR Filter Design
5. Adaptive Signal Processing
6. Lab. using DSP Processor and FPGA

About the Department of the Electronics and Communication Engineering

The Department of Electronics and Communication Engineering started in the year of 1996 with an intake of 60 and later increased to

120 seats. The department of ECE conducts M.Tech in Electronics and Communication Engineering with an intake of 18 seats and it also provides various certification courses in VLSI and Embedded systems to UG as well as PG students. This AICTE sponsored FDP will be organized by the Department of ECE with modern state-of-the-art VLSI and Embedded system laboratory equipped with industry standard EDA tools, measurement set up having Agilent's Vector Network Analyzer (upto 20GHz), Semiconductor parameter Analyzer, Microstrip antenna fabrication unit etc., sponsored by DST, Govt. of India under DST-FIST program. Apart from this the department also has various other laboratories such as Microprocessor and Microcontroller Lab, Analog Electronics Lab, Microwave communication Lab, Signal Processing Lab etc. The department is also enriched by high profile faculties from various reputed institutions all over the country and abroad and are engaged in various areas of research like Microelectronics, Modeling of Semiconductor Devices, Signal processing using FPGA, VLSI based Embedded System, Microstrip Antenna design and fabrication, Optical Technology etc. The department has published more than 10 papers in last two years in International Journals of repute (including IEEE and IEE) and more than 20 Conference papers in last two years. The department has produced two BOYSCAST fellow and one Samanta Chandrasekhar Young Scientist Awardee.

ABOUT NIST

National Institute of Science & Technology was set up in 1996. It is approved by AICTE and affiliated to BPUT. The institute campus of 60 acres is located at picturesque Palur hills about 12 kms from Berhampur and 3 hours drive from Bhubaneswar. **The institute is headed by Prof.**

Sangram Mudali (M.S., University of Houston, B.Tech., IIT Kanpur, B.F. Goodrich, Ohio). About 2500 students are studying in various disciplines of M.Tech, B.Tech., MCA, MBA & PGDM at NIST. The entire campus is 24X7 WLAN connected.

Within a short span of 13 years, NIST has produced 4 BOYSCAST scholars, 2 Full Bright scholars, 3 Samant Chandrasekhar Young Scientist Awardees and more than 20 Post-Doctorate scholarships have been awarded to the institute faculty. Our faculty members have published more than 15 books and 50 papers in international journals of repute. The institute received a number of grants from AICTE, DST, UGC, MHRD, CSIR and private IT companies for projects, Staff and Faculty Development Programmes. **In 2009, NIST received a grant of about Rs. 25 Lakhs to conduct FDPs/SDPs in Advanced Computer Control of Industrial Process and Automation Engineering, Advanced DSP implementation using FPGA & DSP Processor, Microelectronics/VLSI Design for Engineers and Scientists, VLSI Design using FPGA & IC station (the next course in this series), EDC Programmes, etc. sponsored by AICTE. Prof. S.P. Pati and Prof. J. Majhi have been appointed by AICTE as Emeritus fellow in NIST for two years.**

NIST students are recruited every year by almost all top notch MNCs and Indian IT sector & Core Engineering sector companies. Students are also recruited by few MNCs like Wipro etc. in VLSI Domain. NIST conducts certification courses during summer vacation on VLSI, ORACLE, Industrial Automation, JAVA, .NET, SAP etc. to groom the students and make them industry ready. The institute has tied up with companies like Infosys, Wipro, Mphasis, TCS etc. for projects and technical certification courses etc. through various Industry-Institute interaction programmes.

APPLICATION FORM

for

**AICTE Sponsored
Faculty Development Program On**

**“Advanced Digital Signal Processing
Implementation Using FPGA & DSP
Processor”**

25th to 31st August 2009

1. Full Name :
(In Block letter)
 2. Designation :
 3. Institute Name & Address :
 4. Whether the Institute has AICTE approval ?
Yes / No
 5. Sex : M / F
 6. Contact Number :
 7. E-mail Id :
 8. Accommodation needed : Yes / No
 9. Signature of the Participant :
- Date:
Place:
- Signature of the Head of the Institute /
Sponsoring Authority
(With Date and Seal)**