Registration

The participation is open to all interested faculties including students of M.Tech. and B.Tech. (3rd and 4th Year) absolutely on pre registration on first come first serve basis as the number of seats is limited. The program starts at 9:00 AM and will continue till 5:00PM every day.

Registration Charges: Free & Free working Lunch during the program

Address For Communication

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Vice-Chancellor BPUT Rourkela, Odisha

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Chairman, NIST Governing Body

Program Convener

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Prof. Manoja K Senapati

Prof. Diganta Das

Prof. Santosh K Acharya

Prof. Arttatran Sahu

Prof. Santosh K Padhi







TEQIP-III BPUT, Odisha Sponsored

3 - Day Workshop

Signal Processing Techniques for 5G Communication



TIFAC -Conference Hall 25th - 27th September, 2019

In Association with

TEQIP Cell Biju Patnaik University of Technology, Odisha, Rourkela

Organized by

DEPT. OF ELECTRONICS & COMMUNICATION ENGG.

NATIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY (Autonomous) Institute Park, Pallur Hills, Berhampur, Odisha, INDIA. Pin: 761008

Ph.: 0680-3925483, Fax: 0680-2492627 Web: www.nist.edu

About Workshop

This Workshop aims at providing strong theoretical background along with practical experience in the field of signal processing techniques in 5G wireless Communications. Leaders in the field will describe state-of-the-art research in applying signal processing methodologies in the context of tomorrow's most important wireless applications, ranging from communication services, to nomadic computing and wireless multimedia.

Representatives of industries and academia have started to look into the technological developments toward the next generation (5G) next to 4G networks. Signal processing Techniques, algorithms and architectures have an increasing important role to play in meeting the central challenges faced in the design of advanced wireless communication systems. This Workshop will be a platform for researchers to discuss the technologies proposed for 5G communication.

About NIST

National Institute of Science and Technology (NIST) is promoted by SM Charitable Educational Trust. The Trust was established in 1995 by Dr. Sukant K. Mohapatra and Ms. Sanjeeta Mohapatra (both are NRIs based in US) as Founder/Donor Trustees. Dr. Sukant Mohapatra is the founder chairman of the Trust and NIST supporting and providing visionary guidance for NIST to become a world class institute. The National Institute of Science & Technology (NIST) was established in the year 1996 by a group of academicians and technocrat educated in the top Institutes of India & abroad, NIST is the first NRI educational venture in the state of Odisha and first engineering colleges under Berhampur University and southern Odisha. The primary objective of the founders was to promote NIST as a center of academic excellence and research in the field of science and technology in their home state of Odisha. Keeping true to its core value, mission and vision, NIST has become a top notch institute in the state and country since its establishment. In last decade, NIST has achieved success in terms NAAC with grade A (3.22), ranked 28 by DataQuest in India, Eindia award for the best teaching learning institute, ranked #1 in Odisha under BPUT and many more.

About Dept. of ECE

Department of Electronics and Communication Engineering accredited by NBA in 2019 is having very dynamic groups in the field of VLSI Design and CAD, Semiconductor Device, Nano Science and Technology, Photonics, Instrumentation & Control, Microwave, Robotics and Embedded Systems. Our department has won the Cadence Design Contest 3 times in a row (2014, 2015 & 2016) under UG and every year our project stands within top 5.

About Dept. of ECE (Continued...)

Research groups have reached a milestone for receiving grants up to 10 Crores and able to establish many centers of excellence that includes TIFAC-CORE for 3G/4G. We have signed MoU with industries and top universities like NJIT for collaborative research activities. Our Department is the authorized training patterner for ESSCI under Skill India in VLSI, Embedded Systems, FPGA. Students from our department have placed in top semiconductor companies like intel, nvidia, Cadence, Mentor Graphics, Sankalap Semiconductor, CoreEL, Entuple Technologies and other IT industries like Wipro, Infosys, TCS.

Resource Persons

Dr. Ganapati Panda

Professor, School of Electrical Sciences, IIT Bhubaneswar

Dr. Amalendu Patnaik

Associate Professor, Electronics and Comm. Engg. IIT Roorkee

Dr. Rutuparna Panda

Professor, Dept of ETC, VSS University of Technology, Burla

Mr. Mrinal Das

VP Analog/RF Engineering, Marquee Semiconductor Inc., Bhubaneswar

Dr. Pradipta Kumar Nanda

Professor, SOA University

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Associate Professor, ETC, VSS University of Technology, Burla

Dr. Saroj Kumar Meher

Associate Professor, IIS, Bangalore

Dr. Mangal Singh

Associate Professor, Dept. of ECE, Nirma University

Mr. Sanjib Acharya

Senior Engineer, General Electronics, Hydrabad

Dr. Ajit Kumar Panda

Professor, Dept of Electronics & Comm. Engineering, NIST

Registration Form

3-Day Workshop on

"Signal Processing Techniques for 5G Communication"

National Institute of Science and Technology (Autonomous)
Institute Park, Pallur Hills, Berhampur, Odisha.

Date: 25th -27th September, 2019

NAME:
Designation:
Institute Name and Address:
Contact Number:
EMail:
IEEE Membership ID (If Available):
Accommodation (Yes/ No):
Category (Tick the Appropriate One):
[a] Research Scholar/ Student
[b] Academician
[c] Industrial Personnel
Date of Registration:
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Signature

*** All the participants are requested to bring the organization ID Card as a proof for registration category.